First Named Inventor	Chun Chen
Serial No.	10/662,074
Filing Date	September 12, 2003
Group Art Unit	2822
Examiner Name	Kevin M. Picardat
Attorney Docket No.	400.181US03

SUPPLEMENTAL
INFORMATION
DISCLOSURBECEIVED
STATEMENT_{FEB} 1 8 4005

OFFICE OF PENTION'S

Title: CONTACTLESS UNIFORM-TUNNELING SEPARATE P-WELL (CUSP) NON-VOLATILE MEMORY ARRAY ARCHITECTURE, FABRICATION AND OPERATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, et seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

As an Office Action has not yet issued in this application, Applicant believes that no fees are due. However, the Commissioner for Patents is hereby authorized to charge any additional fees to Deposit Account No. 501373.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2207.

Respectfully submitted,

Date: 91°

Andrew C. Walseth Reg. No. 43,234

Attorneys for Applicant Leffert Jay & Polglaze P.O. Box 581009 Minneapolis, MN 55458-1009 T 612 312-2200 F 612 312-2250

First Named Inventor	Chun Chen		
Serial No.	10/662,074	INFORMATION DISCLOSURE	
Filing Date	September 12, 2003		
Group Art Unit	2822	STATEMENT	
Examiner Name	Kevin M. Picardat	FORM PTO-1449	
Confirmation No.	9792		
Attorney Docket No.	400.181US03	RECEIVED	
Title: CONTACTLESS UNIFORM-TUNNELING		Sheet 1 of 1	
SEPARATE P-WELL (CUSP) NON-VOLATILE		FEB 1 8 2005 Sheet 1 of 1	
MEMORY ARRAY ARCHITECTURE,			
FABRICATION AND	OPERATION	ARRIVE OF PETITIONS	

Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
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Foreign Patent References					
Examiner	Foreign Pat	ent	Name	Publication	Translation
Initials	Country	No.		Date	

	Other References
Examiner	Author, Title, Date, Pages, etc.
Initials	Li, et al. A Novel Uniform-Channel-Program-Erase (UCPE) Flash EEPROM Using an Isolated P-well Structure 2000
	IEEE (4 pgs.)

Examiner	Da			
Exammer	l Da	⁷		
Signature	Co	sidered		
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not				